

800V Super Junction MOSFET
●General Description

The SJ MOSFET LH80R280 has the low $R_{DS(on)}$, gate charge, fast switching and excellent avalanche characteristics. This device offers extremely fast and robust body diode, and is suitable for telecom and power supplies.

●Features

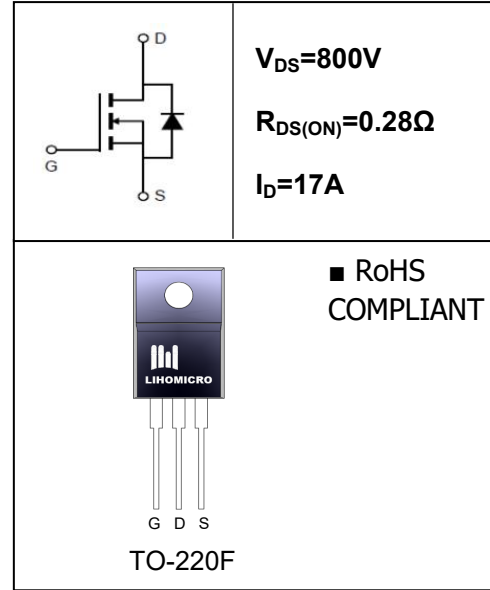
- Low Thermal Resistance
- Fast Switching
- High Input Resistance

●Application

- LED/LCD/PDP TV and monitor Lighting
- Power Supplies

●Ordering Information:

Part number	LH80R280
Package	TO-220F
Basic ordering unit (pcs)	1000
Normal Package Material Ordering Code	LH80R280F-T0220F-TU
Halogen Free Ordering Code	LH80R280F-T0220F-TU-HF


●Absolute Maximum Ratings (TC =25℃)

PARAMETER		SYMBOL	Value	UNIT
Drain-Source Breakdown Voltage ¹		BV_{DSS}	800	V
Gate-Source Voltage		V_{GS}	±30	V
Continuous Drain Current	TC = 25℃	I_D	17	A
	TC = 100℃		11	
Pulsed drain current (TC = 25℃, tp limited by Tjmax) ^{2,4}		I_{DM}	51	A
Single Pulse Avalanche Energy ²		E_{AS}	720	mJ
Power Dissipation(TC=25℃)		P_D	168	W
Peak Diode Recovery dv/dt ³		dv/dt	15	V/ns
Junction Temperature		T_J	-55~+150	℃
Storage Temperature		T_{STG}	-55~+150	℃

●Electronic Characteristics

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	800	--	--	V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5	--	4.5	V
Drain-source On Resistance ³	$R_{DS(ON)}$	$V_{GS}=10V, I_D=11A$	--	0.24	0.28	Ω
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=800V, V_{GS}=0V, T_J=25^\circ C$	--	--	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V$	--	--	± 100	nA
Input Capacitance	C_{iss}	$V_{GS}=0V,$ $V_{DS}=25V,$ $f=1.0MHz$	--	1828	--	pF
Output Capacitance	C_{oss}		--	1480	--	
Reverse transfer Capacitance	C_{rss}		--	52	--	
Turn -Off Delay Time ³	$T_d(off)$	$V_{DD}=400V,$ $I_D=17A,$ $V_{GS}=10V$ $R_G=25\Omega$	--	160	--	ns
Turn -On Delay Time ³	$T_d(on)$		--	28	--	
Rise Time	T_r		--	61	--	
Fall Time	T_f		--	50	--	
Total Gate Charge	Q_g	$I_D=17A,$ $V_{DS}=640V$ $V_{GS}=0\sim 10V$	--	56	--	nC
Gate-to-Source Charge	Q_{gs}		--	10.8	--	
Gate-to-Drain Charge	Q_{gd}		--	22	---	

●Reverse Diode Characteristics

Continuous Diode Forward Current	I_S	--	--	--	17	A
Pulsed Diode Forward Current	I_{SM}	--	--	--	51	A
Diode Forward Voltage	V_{SD}	$I_S=17A$ $V_{GS}=0V$	--	--	1.4	V
Reverse Recovery Time	t_{rr}	$I_f=11A$ $di_F/dt=100A/\mu s$	--	548	--	ns
Reverse Recovery Charge	Q_{rr}		--	9.5	--	μC

●Thermal Characteristics

PARAMETER	SYMBOL	MAX	UNIT
Thermal Resistance Junction-case	R_{thJC}	0.69	$^\circ C/W$
Thermal Resistance Junction-ambient	R_{thJA}	62.5	$^\circ C/W$

Notes:

1.Repetitive Rating: Pulse width limited by maximum junction temperature.

2. $I_{AS}=7A, V_{DD}=25V, R_G=25\Omega, L=10Mh$, Starting $T_J=25^\circ C$

3. Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

• **Typical Characteristics**

Figure 1. Output Characteristics

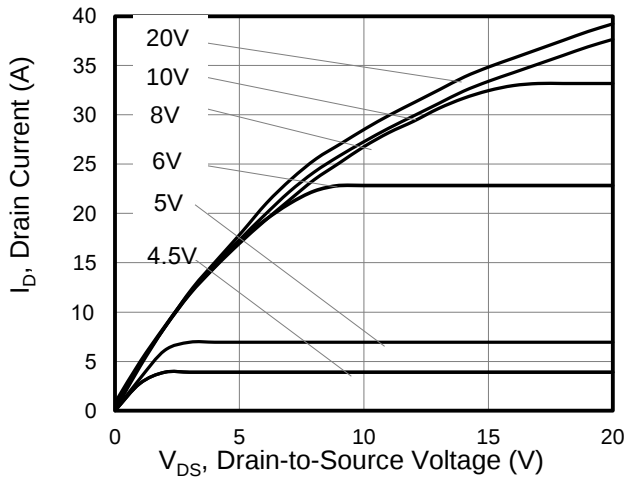


Figure 2. Transfer Characteristics

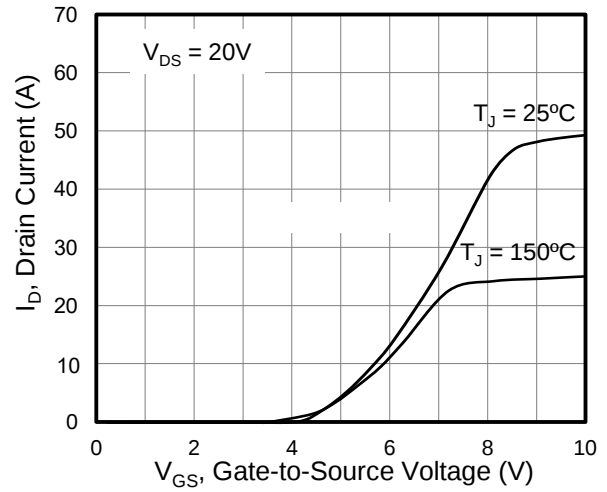


Figure 3. On-Resistance vs. Drain Current

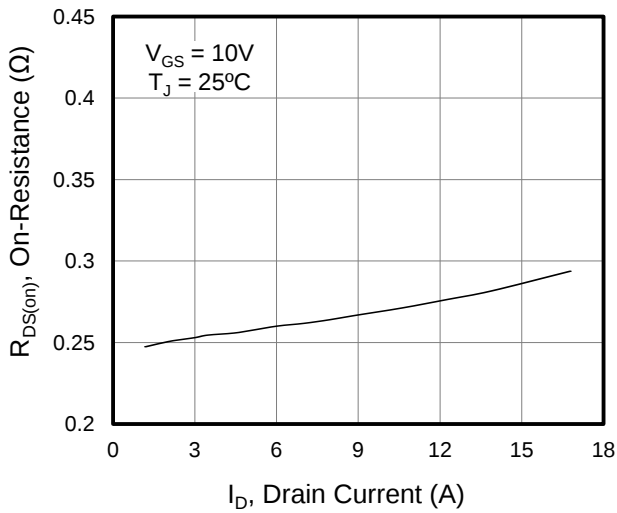


Figure 4. Capacitance

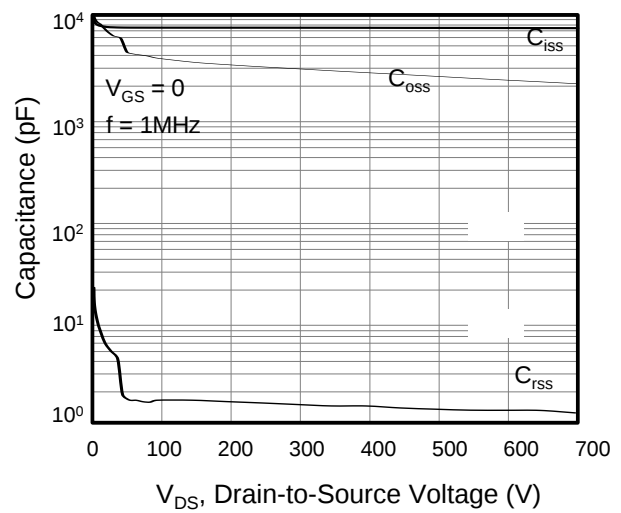


Figure 5. Gate Charge

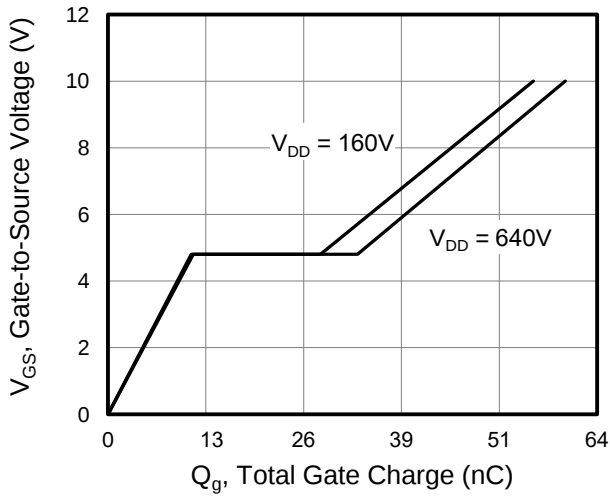
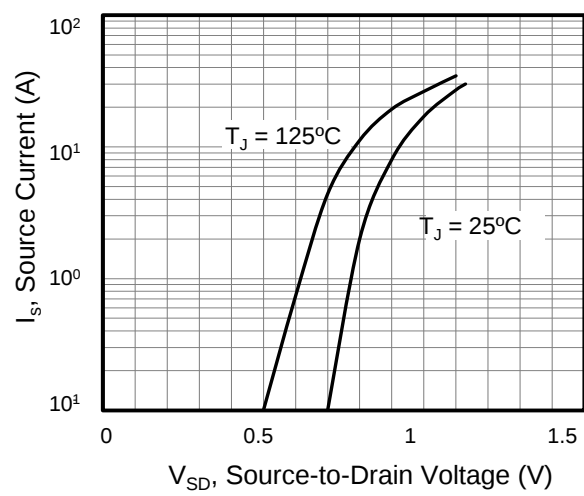


Figure 6. Body Diode Forward Voltage



●Typical Characteristics(Cont.)

Figure 7. On-Resistance vs. Junction Temperature

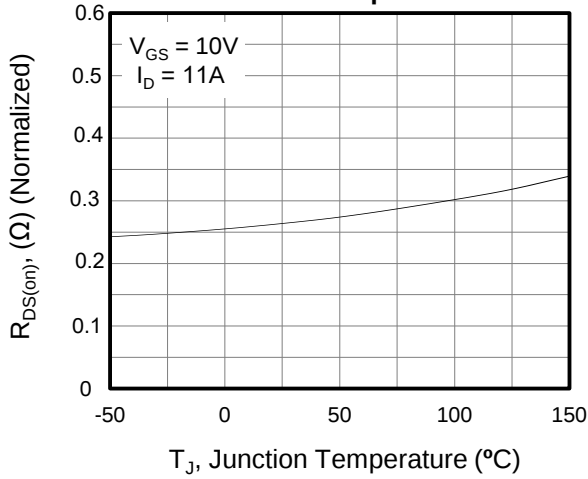


Figure 8. Breakdown voltage vs. Junction Temperature

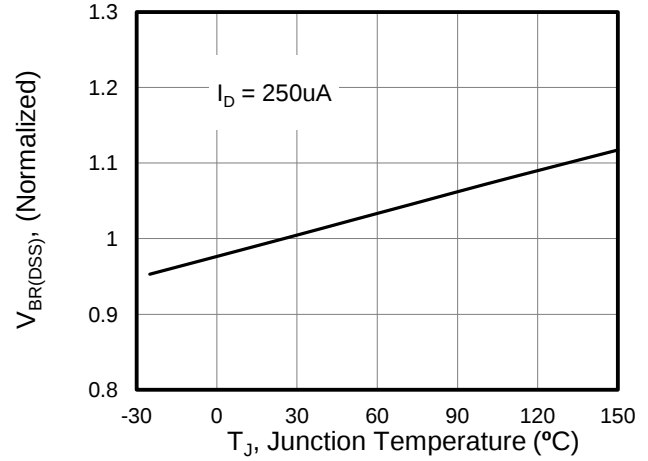


Figure 9. Transient Thermal Impedance

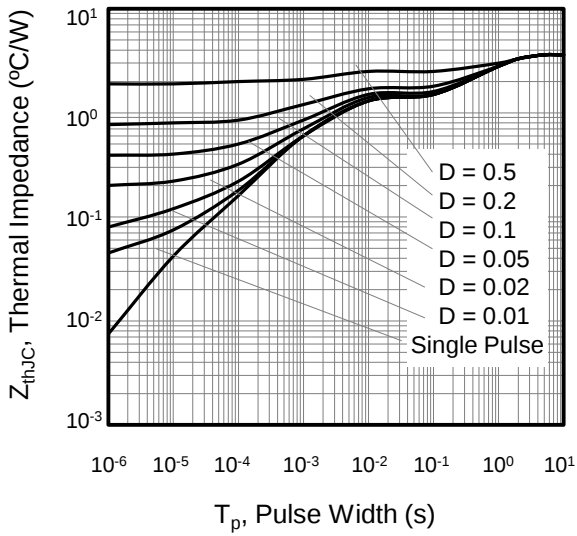
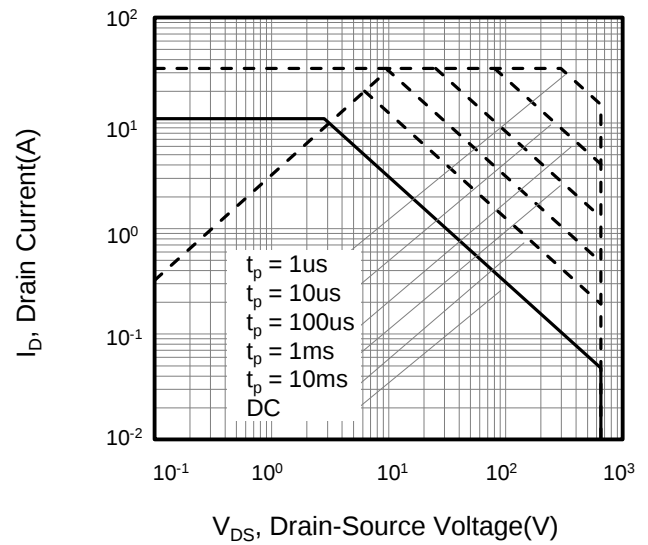


Figure 10. Safe operation area



• **Test Circuit and Waveforms**

Figure A: Gate Charge Test Circuit and Waveform

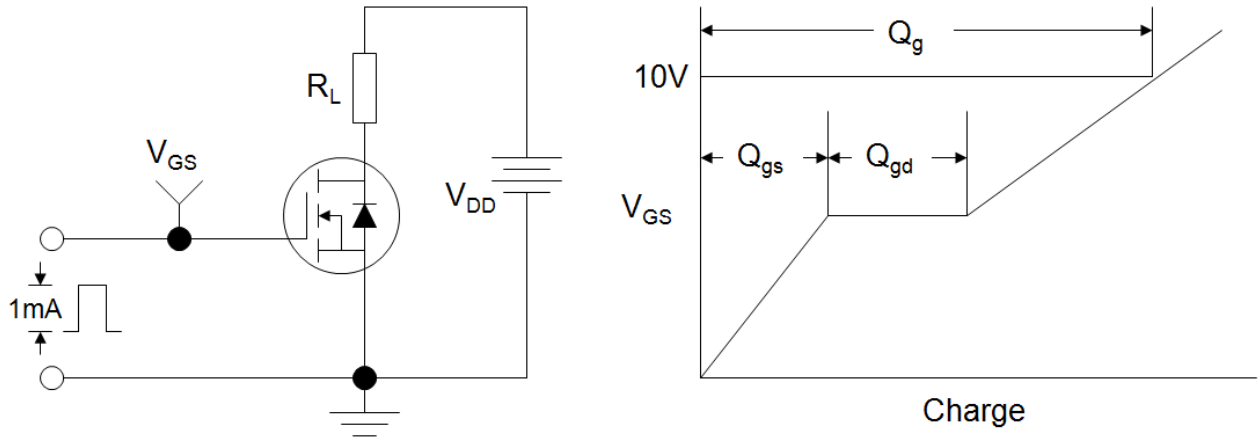


Figure B: Resistive Switching Test Circuit and Waveform

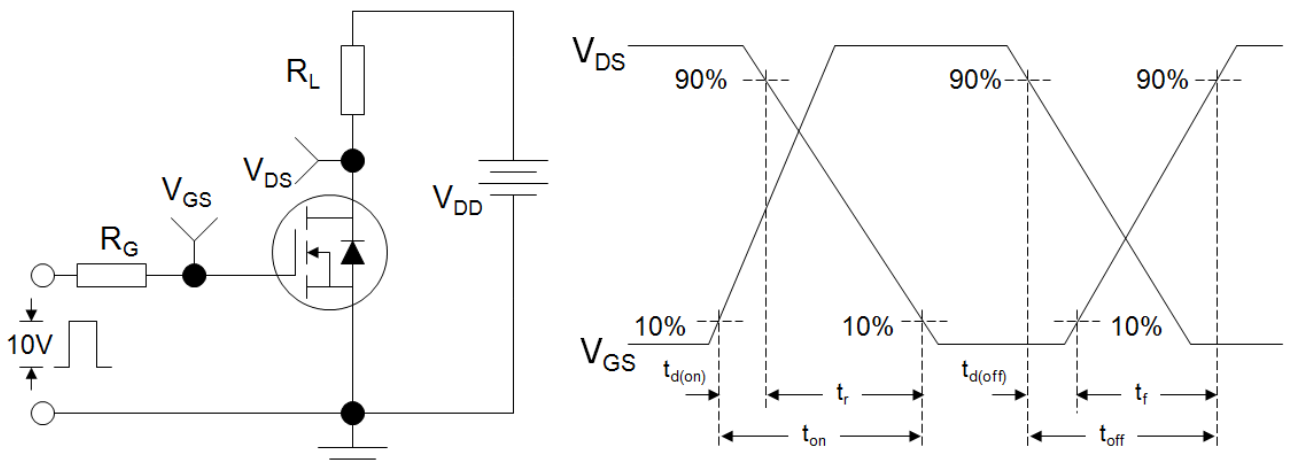
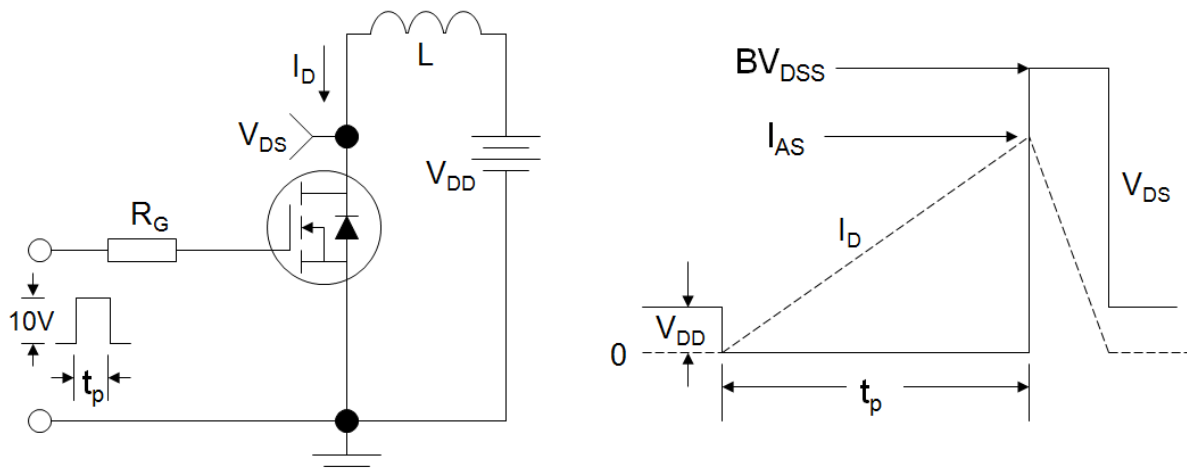


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



●Dimensions (TO-220F)

SYMBOL	min	max	SYMBOL	min	max
A	4.20	4.80	E1	8.30	8.70
A1	2.50	2.90	e	2.40	2.70
A2	2.90	3.30	e1	4.95	5.25
b	0.40	0.80	F	2.50	2.90
b1	1.10	1.50	L	13.00	14.00
c	0.50	0.70	L1	3.00	4.00
D	9.80	10.60	∅P	2.90	3.50
E	14.60	15.60			

